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Total Number of Pages in This Submission

Application Number	10/717,344
Filing Date	November 18, 2003
First Named Inventor	Maxim Pribytko
Art Unit	2817
Examiner Name	Khanh V. Nguyen
Attorney Docket Number	X-1442 US

ENCLOSURES (Check all that apply)		
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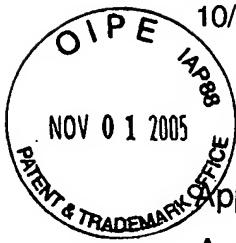
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X-1442 US
10/717,344

PATENT
Conf. No.: 8185



IN THE UNITED STATES PATENT OFFICE

Applicants: Maxim Pribylko et al.
Assignee: Xilinx, Inc.
Title: Method and Apparatus for Providing High Common-Mode Rejection Ratio in a Single-Ended CMOS Operational Transconductance Amplifier
Serial No.: 10/717,344 File Date: 11-18-03
Examiner: Khanh V. Nguyen Art Unit: 2817
Docket No.: X-1442 US Conf. No.: 8185

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AMENDMENT IN RESPONSE TO THE FINAL OFFICE ACTION

Dear Sir:

In response to the Final Office Action mailed from the Patent Office on August 30, 2005, please replace/substitute the following claims as indicated.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 8 of this paper.